

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,912,882 B2
APPLICATION NO.: 10/561,558
ISSUE DATE : March 22, 2011
INVENTOR(S) : Kim

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4, line 45, in Claim 1, delete "into DDS" and insert - - into a DDS - -.

Column 4, line 57, in Claim 1, delete "click" and insert - - clock - -.

Column 4, line 58, in Claim 1, delete "multipler" and insert - - multiplier - -.

Column 4, line 60, in Claim 1, delete "frequency," and insert - - frequency; - -.

Column 4, line 65, in Claim 1, delete "analog the" and insert - - analog - -.

Column 5, line 2, in Claim 2, delete "10x PLL" and insert - - 10xPLL - -.

Column 5, line 4, in Claim 3, delete "below" and insert - - below, - -.

Column 5, line 18, in Claim 6, delete "bandpass" and insert - - band pass - -.

Column 6, line 8, in Claim 12, delete "phase a" and insert - - phase - -.

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